



PATENT

-1-

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Patent Application of

Inventor(s): Yuri Mirgorodski et al.

Application No.: 10/665,187

Filed: September 17, 2003

For: METHOD OF ERASING AN EEPROM  
CELL UTILIZING A  
FREQUENCY/TIME DOMAIN BASED  
ERASED SIGNAL

Group Art Unit: Unknown

Examiner: Unknown

**TRANSMITTAL OF FORMAL  
DRAWINGS**

121 Spear Street, Suite 290  
San Francisco, CA 94105  
(415) 512-1312

Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**CERTIFICATE OF MAILING**

I hereby certify that this correspondence is being deposited  
with the United States Postal Service as First Class Mail in an  
envelope, addressed to: Commissioner for Patents, P.O.  
Box 1450, Alexandria, VA 22313-1450 on December 1,  
2003.

STALLMAN & POLLOCK LLP

Dated: 12/1/03

By: 

Janet Chan

Sir:

Transmitted herewith are five (5) sheets of formal drawings to be filed in the above-identified application.

Respectfully submitted,

STALLMAN & POLLOCK LLP

Dated: December 1, 2003

By: 

Michael J. Pollock

Reg. No. 29,098

Attorneys for Applicant(s)